Rethinking the SDN Abstraction

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- Separate Control and Data
- Abstraction
 Global view





OPEN NETWORKING FOUNDATION

OF-PI: A Protocol Independent Layer

Version 1.1 September 5, 2014

ONF TR-505

POF: Protocol Oblivious Forwarding

- Table search keys are <u>Match</u> defined as {offset, protocols/headers length} tuples
- Instructions/Actions access packet data or metadata using {offset, length} tuples
- Include other math, logic, move, branching, and jump instructions

~40 matching header fields defined yet still many uncovered

OFPAT COPY TTL OUT **Action** OFPAT_COPY_TTL_IN OFPAT SET MPLS TTL OFPAT DEC MPLS TTL **OFPAT PUSH VLAN** OFPAT POP VLAN **OFPAT_PUSH_MPLS** OFPAT_POP_MPLS OFPAT SET NW TTL OFPAT DEC NW TTL **OFPAT PUSH PBB OFPAT POP PBB** and on and on and on ... **Current OpenFlow** {offset, length} covers any frame based formats

POFAT SET FIELD POFAT ADD FIELD POFAT DELETE FIELD POFAT MOD FIELD Period.

<u>POF</u>

Packet field parsing and handling are abstracted as generic instructions to enable flexible and future proof forwarding elements. This is simple yet has profound implications to SDN.

Haoyu Song, Protocol-Oblivious Forwarding: Unleash the Power of SDN through a Future-Proof Forwarding Plane @ HotSDN 2013

P4: Programming Protocol-Independent Packet Processors



Proposed by

- -Nick McKeown
- -Jennifer Rexford
- -Amin Vahdat
- -George Varghese

Goals

- -Protocol independence
- -Target independence
- -Reconfigurability

"P4: Programming Protocol-Independent Packet Processors," *ACM Sigcomm Computer Communications Review (CCR). Volume 44, Issue #3 (July, 2014)*

Application-Driven Network





Application Driven Network: providing On-Demand Services for Applications. Demo&Poster @ **SIGCOMM '16** (co-work with Huawei).



POF/P4 is flexible enough?

Case 0: programmable SDN Data Plane



ONetCard 2012 Aug PCle Card



ONetSwitch 45 4*10G, 4*GE, wifi 2013 Aug



ONetSwitch 20 4*GE, with ZEDboard 2013 Dec



ONetSwitch 30 wifi/storage, 5*GE 2014 Dec.

Sponsored by Xilinx

http://onetswitch.org

ONetSwitch: All programmable SDN Switch

Chengchen Hu, Ji Yang, Hongbo Zhao, and Jiahua Lu. "Design of all programmable innovation platform for software defined networking". **Open Networking Summit (ONS) 2014**, Santa Clara, CA, US, 2014





Chengchen Hu, Ji Yang, Zhimin Gong, Shuoling Deng, Hongbo Zhao. "DesktopDC: Setting All Programmable Data Center Networking Testbed on Desk", **Poster&Demo at SIGCOMM 2014**, Chicago, IL, US, 2014

Users



400+ ONetSwitches deployed in China, Europe, US

Case I: Frequent protocols



Control

DATA



Data plane packet goes to controller repeatedly even topology is stable Usually 2-5s per port per packet

Control

DATA

Case 3: LACP (Link aggregation Control Protocol) State maintenance Repeat the same work again and again after topology is stable Usually 8 packets to converge Usually 1/30s per port per packet after convergence



Communication overhead reduced: 50%-100% Controller CPU work load reduced: 80%-98% ARP response time: from 10+ms to us FOCUS: Function Offloading from a Controller to Utilize Switch Power. **Poster @ NSDI'16 and @ SDN/NFV workshop'16**

Case2: Table-miss



Repeated flowmod message: 68%-98% when flow table with1k-50k entries. Eat either fast path memory or bandwidth between switch and controller Becomes bottleneck between Slow path and fast path in switch Small flows make the problem worse.

CoSwitch: A Cooperative Switching Design for Software Defined Data Center Networking **@HotData, 2014** (best paper award) Co-Work with IBM Research Lab Taming the Flow Table Overflow in OpenFlow Switch. **Poster at SIGCOMM '16**.

Case 3: Rules Conflicts



Hard to eliminate the conflicts without the high-level intents

- blue and orange policies are only to connect A and D → random overwriting
- blue and orange policies critically specify the current path → unsolvable
- blue/orange policy is to connect A and with B/C in path → A to B to C to D

Modular SDN Compiler Design with Intermediate Representation. **poster @ SIGCOMM '16**.

{A, DstIP 10.0.0.2}, FORWARD	, towards B \cap forbid A
{A, DstIP 10.0.0.2}, FORWARD	, towards C \cap forbid A
{ <i>B</i> , DstIP 10.0.0.2}, FORWARD	, towards D \cap forbid B
{ <i>C</i> , DstIP 10.0.0.2}, FORWARD	, towards D \cap forbid C
{ <i>D</i> , DstIP 10.0.0.2}, FORWARD	, fixed_forward 2
{ <i>D</i> , DstIP 10.0.0.2}, FORWARD	, fixed_forward 2







Case 4: Rule update

- Flowtable update bottleneck
 - 10s to 100s of rule edits per second
 - Full refresh of 5K entries takes minutes



Try to minimize the update



Xitao Wen, Bo Yang, Yan Chen, Li Erran Li, Kai Bu, Peng Zheng, Yang Yang, Chengchen Hu, RuleTris: Minimizing Rule Update Latency for TCAM-based SDN Switches, **ICDCS 2016**, Nara, Japan, June 27 – June 30, 2016.

Case 5: SDN Counters

OpenFlow Controller	
OpenFlow Switch	
Slow Path	CPU 30.00%
OpenFlow Channel	20.00% 10.00% 10% 10.00%
Hardware Management Driver / Control Algorithm	0.00% Usage CPU 2GHz ■ DRAM 2GB ■ SRAM/OCM 512K
Fast Path	FPGA/ASIC Chip Size 54.3% Match Memory 37% IO, Buffer, Queue
Parser Counter Group Table Meter	 7.4% Action Engine 1.3% Parser +Extra 49.6% Counter
Flow Table 0 1 Flow Table N	

CACTI: CAche CounTIng



Case 6: Abstract L7

We propose a new specification form DCCFG, which can be formulated as a six-tuple, $\Gamma = (\mathbb{N}, \Sigma, \mathbb{C}, \mathbb{R}, S, \mathbb{E})$, where \mathbb{N} , Σ , \mathbb{C} , \mathbb{R} , S, \mathbb{E} are the finite set of non-terminals, terminals, counters, production rules, start non-terminal, and extraction tokens, respectively. The non-terminals are the symbols where the terminals can be derived. The terminals can be a single character or a RegEx. The production rules can be described as <guard>:<non-terminal> \rightarrow <body><action>.

1	\mathbf{S}	\rightarrow T L V
2	Т	\rightarrow <key, "[a-z]"="" 0,=""></key,>
3	L	\rightarrow "[0-9]" [len=getnum()]
4	[len>0]V	\rightarrow <value,0, "[a-za-z]*"=""> [len=reduce()]</value,0,>
5	[len=0]V	$\rightarrow \epsilon$

Figure 3: A TLV specification Γ in DCCFG.



Parsing Application Layer Protocol with Commodity Hardware for SDN@ACM/IEEE ANCS 2015

Case 7: Vulnerabilities



On Denial of Service Attacks in Software Defined Networks @ IEEE Network SDNShield: Reconciliating Configurable Application Permissions for SDN App Markets (DSN 2016 Mind the Gap: Monitoring the Control-Data Plane Consistency in Software Defined Networks @ CoNext 2016

Open Questions

- How to make data plane programable ?
- Flow-action abstraction?
- Forwarding—Switch, Control—Controller?
- Fully Centralization?
- Anything between high level intents and low level rules?
- How to co-design Fast Path & Slow Path in Switch

Thank you